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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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Takatomo Nagamine

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EXAMINER

TRAN, TRANG U

ART UNIT

PAPER NUMBER

2622

MAIL DATE

DELIVERY MODE

07/02/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/511,733

Applicant(s)

TAKATOMO NAGAMINE ET AL.

Examiner

Trang U. Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 November 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4 and 7 is/are rejected.
- 7) ☒ Claim(s) 5 and 6 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
 - 2) ☐ Certified copies of the priority documents have been received in Application No. _____.
 - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date 3/11/05;10/20/05;1/26/07.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-4 and 7 are rejected under 35 U.S.C. 102(e) as being anticipate by Yang et al. (US Patent No. 6,833,875 B1).

In considering claim 1, Yang et al discloses all the claimed subject matter, note
1) the claimed analog/digital converting means, for inputting a composite video signal of a first system with a first color burst signal frequency, said first system and said first color burst signal frequency being different than a second system having a second color burst signal frequency, and for converting an inputted analog composite video signal to a digital composite video signal by sampling with a sampling frequency in accordance with a system clock is met by the A/D converter 116 (Fig. 1, col. 5, line 13 to col. 6, line 30), 2) the claimed video signal processing means for executing a YC separation process for separating a luminance signal and a chroma signal from said digital composite video signal, and for executing a chroma demodulation process for demodulating said chroma signal obtained by said YC separation process, at a predetermined timing based on said system clock is met by the Y/C separator 130 and

the demodulator 140 (Fig. 1, col. 5, line 13 to col. 6, line 30 and col. 10, line 12 to col. 12, line 64), and 3) the claimed system clock generating means for generating said system clock synchronized with a color burst signal extracted from said digital composite video signal, and configured to change and set a coefficient n in accordance with a system of said composite video signal inputted to said video signal processing means so that a frequency m falls in a predetermined range between said first and second systems, in a case where a frequency of said color burst signal is defined as f_{sc} , a coefficient is defined as n , and a frequency m of said system clock is represented by $f_{sc} \times n = m$ is met by the resampler 120 which receives video samples at the sample rate f_{samp} and generates resampled video samples at a first resampled rate $f_{R1} = m.f_{sc}$ (Figs. 1 and 4A, col. 7, line 37 to col. 8, line 44).

In considering claim 2, the claimed low-pass filter means, having a cut-off frequency set in accordance with said sampling frequency of said analog/digital converting means, for passing said inputted analog composite video signal through a band under said cut-off frequency to said analog/digital converting means is met by the low pass filter 410 (Figs. 1 and 4A, col. 7, line 37 to col. 8, line 44).

In considering claim 3, the claimed further comprising: low-pass filter means, to which said digital composite video signal outputted from said analog/digital converting means is inputted, for passing said inputted composite video signal through a band under a predetermined cut-off frequency to said video signal processing means is met by the low pass filter 410 (Figs. 1 and 4A, col. 7, line 37 to col. 8, line 44).

In considering claim 4, Yang et al discloses all the claimed subject matter, note 1) the claimed further comprising: a determination circuit for determining said first system or said second system of said inputted composite video signal, based on a state of synchronization with said color burst extracted from said composite video signal when said system clock frequency is switched is met by the timing circuit 124 (Figs. 1 and 4A, col. 7, line 11 to col. 9, line 6), and 2) the claimed signal switching means for outputting a composite video signal after conversion to said digital signal by said analog/digital converting means, instead of said luminance signal obtained by said video signal processing means, during said determination operation executed by said determination circuit is met by the input resampler 120 (Figs. 1 and 4A, col. 7, line 37 to col. 8, line 44).

The method claim 7 is rejected for the same reason as discussed in claim 1 above.

Allowable Subject Matter

3. Claims 5-6 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 5 identifies the distinct features: "wherein said video signal processing means is configured further comprising decimating sample means provided at a previous stage of said video signal processing means, for executing a sampling process on said inputted composite video signal as a digital signal based on a decimating rate determined by a relation between said coefficient a and said coefficient n". The closest

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prior art, Yang et al. (US Patent No. 6,833,875 B1), either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Claim 6 identifies the distinct features: "wherein further comprising analog/digital inverting means corresponding to said component signal, which is provided every predetermined number of signals forming said component signal, for converting an inputted analog composite video signal to a digital composite video signal by sampling with a sampling frequency in accordance with a system clock of said frequency b; and low-pass filter means corresponding to a component signal, which is provided at a previous stage of said analog/digital inverting means corresponding to a component signal, for passing an inputted signal through a band under a cut-off frequency set based on a sampling frequency of said analog/digital inverting means corresponding to a component signal; wherein said coefficient n is set so that a system clock having a frequency m generated by said system clock generating means has a frequency difference that falls in a predetermined range with respect to said frequency b". The closest prior art, Yang et al. (US Patent No. 6,833,875 B1), either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Yoshii et al. (US Patent No. 7,224,407 B2) disclose color demodulation device, color demodulation method and image display device.

Yamaguchi et al (US Patent No. 6,441,860 B1) disclose video processing apparatus.


Miyazaki et al. (US Patent No. 6,285,402 B1) disclose device method for converting scanning.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trang U. Tran whose telephone number is (571) 272-7358. The examiner can normally be reached on 8:00 AM - 5:30 PM, Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Ometz can be reached on (571) 272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

June 24, 2007



Trang U. Tran
Primary Examiner
Art Unit 2622